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lvex.[s/u].[ms].[ds] rD, rA, rB

 Γ

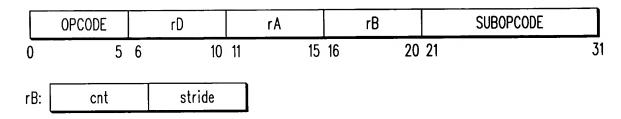


FIG. 2

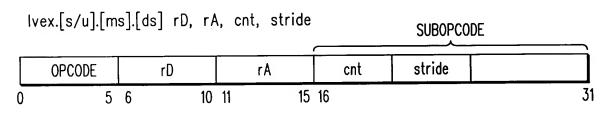


FIG. 3

Imvex.[s/u].[ms].[ds] rD, rA, rB

ſ	OPCODE	rD	rA	rB	SUBOPCODE
٠.					

rB:	cnt	stride	skip	skip_cnt

FIG. 4

Imvex.[s/u].[ms].[ds] rD, rA, cnt, stride, skip, skip_cnt

OPCODE rD rA cnt stride skip skip_cnt

Imvex2.[s/u].[ms].[ds] rD, rA, rB

 Γ

OPCODE	rD	rA	rB	SUBOPCODE

rB: cnt rcnt stride skip

FIG. 6

Istrmvex.[s/u].[ms].[ds] rD, rA, rB

OPCODE rD rA	rB SUBOPCODE	
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rB: cnt rcnt stride skip skip_cnt

FIG. 7

 $stvex.[s/u].[ms].[ss].[h/I] \ rS, \ rA, \ rB$

				CUDODOODE
OPCODE	rS	rA	rB	SUBOPCODE

rB: cnt stride

 $stmvex.[s/u].[ms].[ss].[h/I] \ rS, \ rA, \ rB$

OPCODE	rS	rA	rB	SUBOPCODE
		·		

rB: cnt stride skip skip_cnt

FIG. 9

stmvex2.[s/u].[ms].[ss].[h/I] rS, rA, rB

OPCODE	rS	rΔ	rB	SUBOPCODE
UPCODE	13	TA	סו	SUBUFCUL

rB: cnt rcnt stride skip

FIG. 10

ststrmvex.[s/u].[ms].[ss].[h/I] rS, rA, rB

OPCODE	rS	rA	rB	SUBOPCODE
OI CODE	13	17	10	30001 00DL

rB: cnt rcnt stride skip skip_cnt

Γ

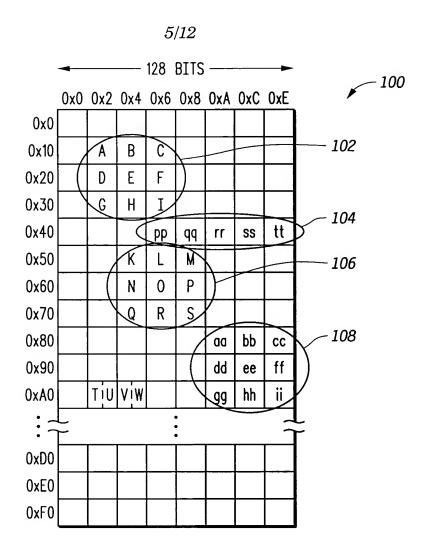
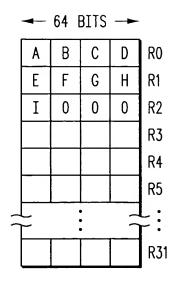


FIG. 12

•	64 E	BITS	-			—	64 (BITS	-	
Α	В	С	0	R0		D	Ε	F	0	R0
K	L	М	0	R1		N	0	Р	0	R1
A+K	B+L	C+M	0	R2		D+N	E+0	F+P	0	R2
				R3		G	Н	I	0	R3
				R4		Q	R	S	0	R4
				R5		G+Q	H+R	I+S	0	R5
			1)	R31))	R31

FIG. 13

FIG. 14



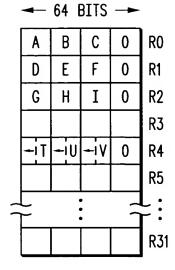


FIG. 16

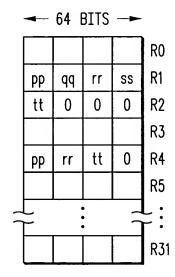


FIG. 17

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7/12 START= 0x26 ۍ ပ TIME=3 S 4 \mathcal{C} ×₀ ۍ TIME=2ی 9 \mathcal{S} 0 ² ى START= 0x24 ပ ડ TIME=1 ٩ \mathcal{C}_{3} 0 ν₀ S START= 7 0x22 - 200 64 BITS + ۍ TIME=0 ۍ 3 മ \mathcal{S} 0 ى ٩ 0x08 0x20 0x28 0x10 0x18 0x30

FIG. 18

→ 64 BITS →								
	0x08							
	0x20							
C ₄	C_4 C_3 C_2 C_1							
c ₀	0	0	c ₀	R7				
X_{-4}	X_3	X ₋₂	X ₋₁	R8				
Х ₀	X ₀ 0 0 0							
$C_4 \cdot X_{-4}$	$c_4 \cdot x_{-4} + c_3 \cdot x_{-3} + c_2 \cdot x_{-2} + c_1 \cdot x_{-1}$							
c ₀	۰x ₀ +0٠٥	0+0•0+0)•0	R11				

Γ

FIG.	19

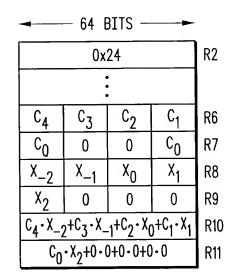


FIG. 21

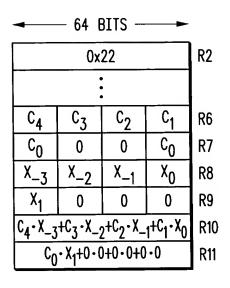


FIG. 20

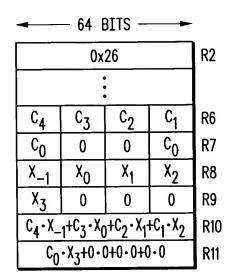


FIG. 22

	→ 64 BITS →						
	Α	В	С	0	R1		
	K	N	Q	0	R2		
	<u> </u>						
	0x12 0x54						

K	N	Q	0	R2				
	R4							
	0x	54	·	R5				
A٠	A·K+B·N+C·Q							
	R11							

→ 64 BITS →

0

Ε

FIG. 23

FIG. 24

← 64 BITS ←						
G	Н	I	0	R1		
K	R2					
	R3					
	R4					
	R5					
A٠	R10					
D.	R11					
	R12					

-	→ 64 BITS →						
G	Н	I	0	R1			
L	L O R O						
				R3			
	0x	:12		R4			
	0x54						
	:						
Α.	A·K+B·N+C·Q						
D٠	D·K+E·N+F·Q						
G.	K+H	·N+I	• Q	R12			

FIG. 25

lmvex_skip_once.[s/u].[ms].[ds] rD, rA, rB

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	OPCODE	rD	rA	rB	SUBOPCODE
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rB: cnt stride skip skip_cnt

FIG. 27

Imvex_cb.[s/u].[ms].[ds] rD, rA, rB

OPCODE	rD	rA	rB	SUBOPCODE

rB: buffer_size offset

FIG. 28

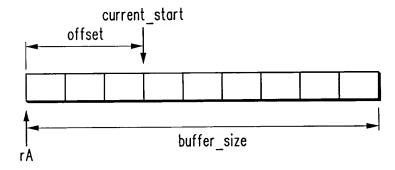


FIG. 29

lstrmvex_cb.[s/u].[ms].[ds] rD, rA, rB

OPCODE	rD	rA	rB	SUBOPCODE
0,0002				

rB: buffer_size offset

 $Imvex_fft.[s/u].[ms].[ds] rD, rA, rB$

OPCODE	rD	rA	rB	SUBOPCODE

rB: radix

Γ

FIG. 31

stmvex_fft.[s/u].[ms].[ss] rS, rA, rB

OPCODE	rD	rA	rB	SUBOPCODE

rB: radix

FIG. 32

 $lmstrmvex_fft.[s/u].[ms].[ds] rD, rA, rB$

OPCODE	rD	rA	rB	SUBOPCODE

rB: radix

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	0x0							0xE	300
0x0									
0x10				Х ₀	X ₁	X ₂	X3	Х ₄	
0x20	X ₅	Х ₆	X ₇						
0x30									
0x40			Y ₀	Y ₄	Y ₆	Y ₂	Y ₁	Y ₅	
0x50	Yz	Y ₇							
0x60									

FIG. 34

Γ

X ₀	X ₄	Х ₆	X ₂	R1
X ₁	X ₅	X3	X ₇	R2
				R3
Y ₀	Y ₁	Y ₂	Y ₃	R4
Y ₄	Y ₅	Y ₆ _	Y ₇	R5

FIG. 35